



The IEEE 8th International Conference on ASIC
TUTORIALS INFORMATION

OCT. 20, 2009, CHANGSHA, CHINA
Press Resort & Hotel

Welcome to Participate ASICON 2009 Tutorials

T-1. **“Assertion-Based Verification”**

Speaker: Yunshan Zhu, CEO of NextTop , USA

T-2. **“High Speed DDR Memory Interface Design”**

Speaker: Brian Zhao, President of Innowave, USA

T-3. **“Tips and Techniques of Power Chip Design”**

Speaker: Shiyang Pei , CEO of SeaWard, USA

T-4. **“Scan Design and DFT Practices”**

Speaker: Linming Jin, Brocade Com., USA